UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO. : 6,965,092 B2 APPLICATION NO.: 10/074287

: November 15, 2005

DATED INVENTOR(S) : Imad Mahawili

> It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4:

Line 40, "semicondector" should be --semiconductor--.

Line 43, "wafers" should be --wafer, --.

Column 6:

Line 60, "speeds" should be --speed,--.

Column 12:

Line 16, Claim 1, "end" should be --and--.

Signed and Sealed this

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Ninth Day of January, 2007

JON W. DUDAS Director of the United States Patent and Trademark Office